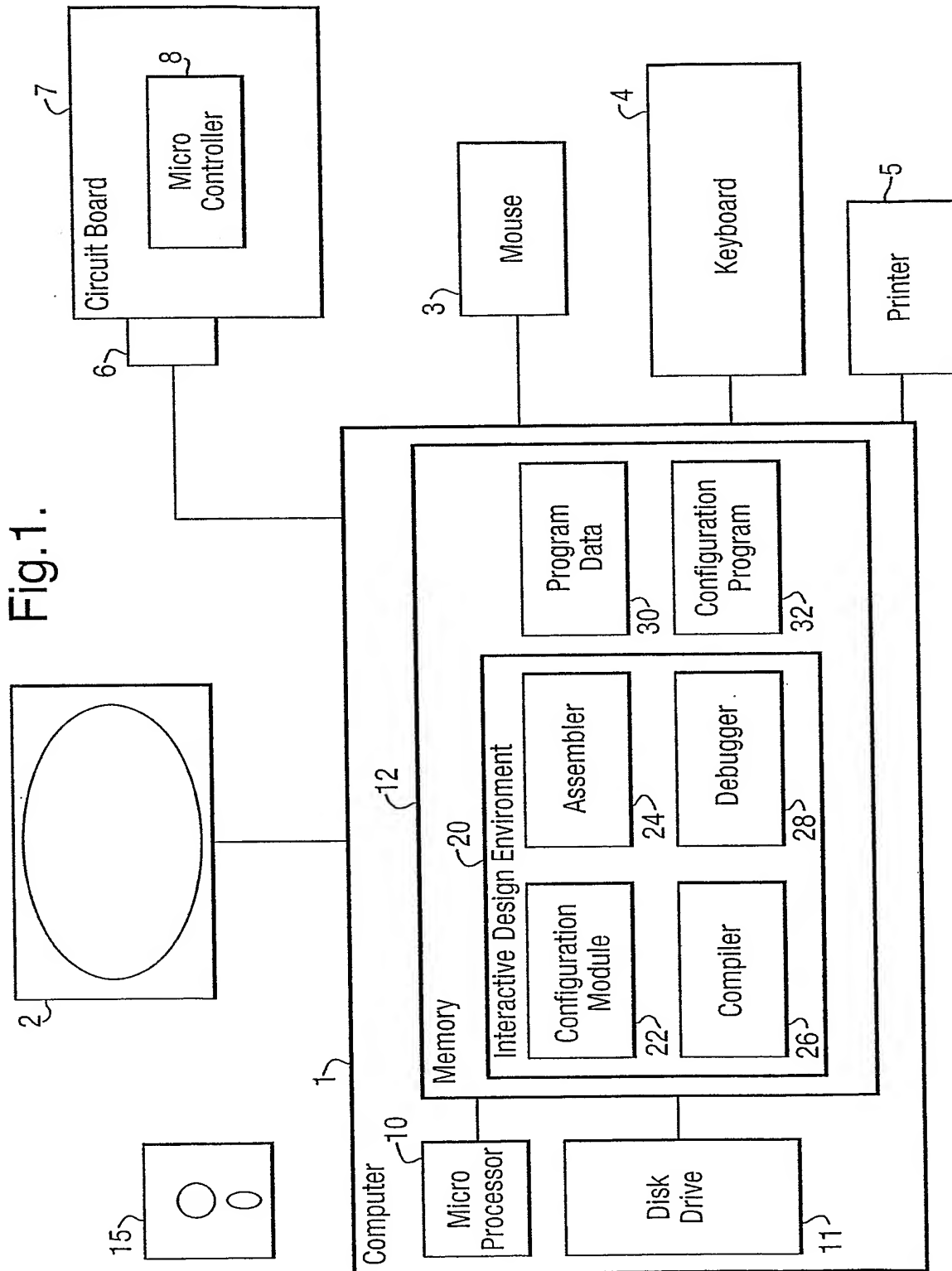
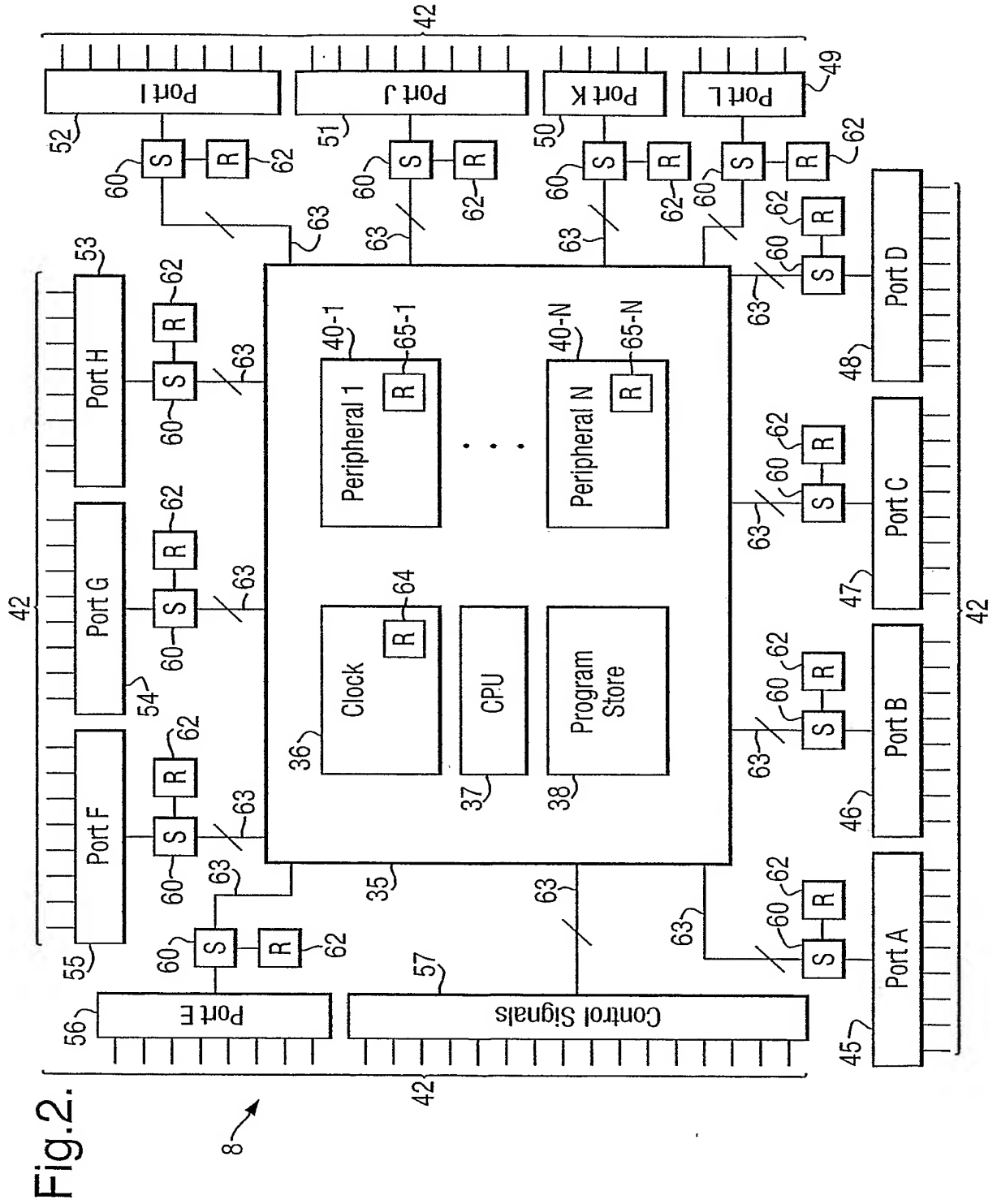


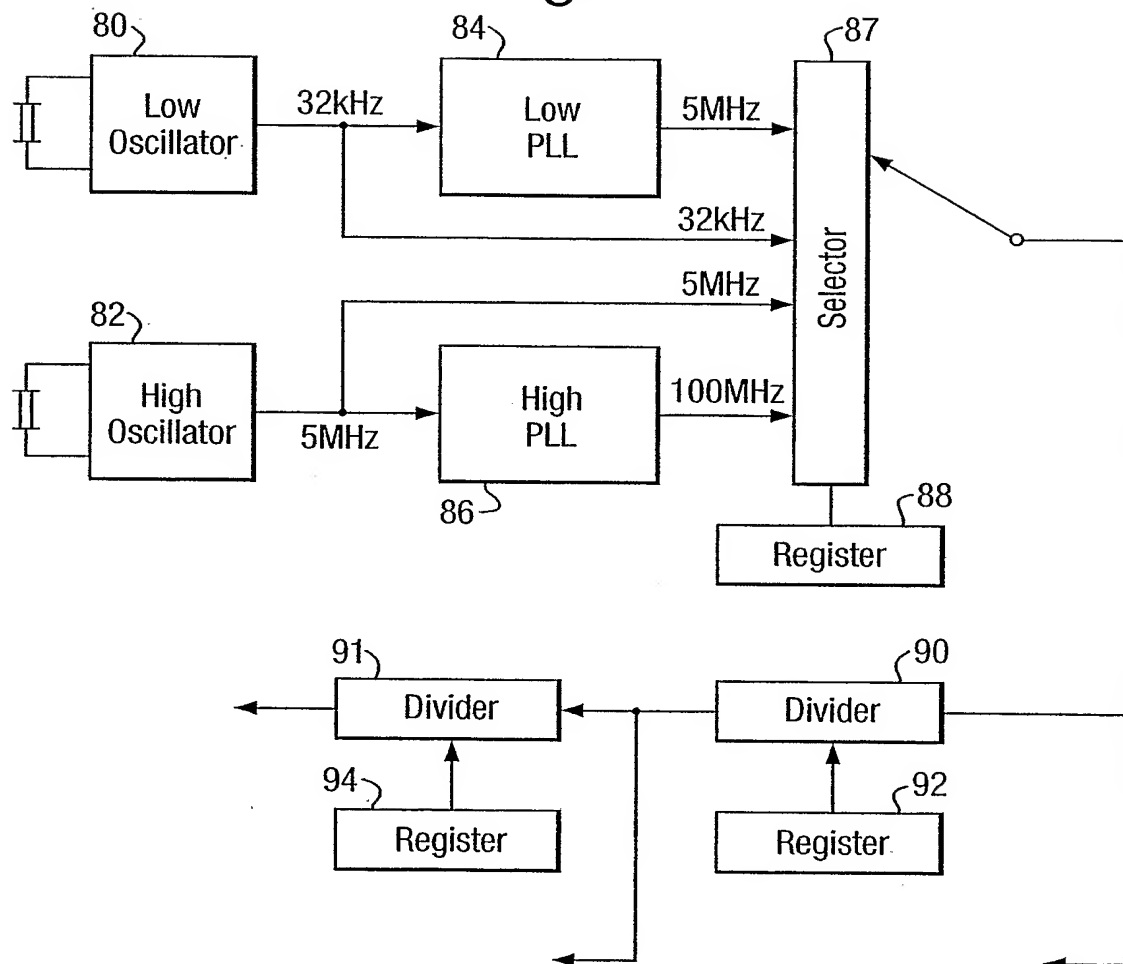
1/20





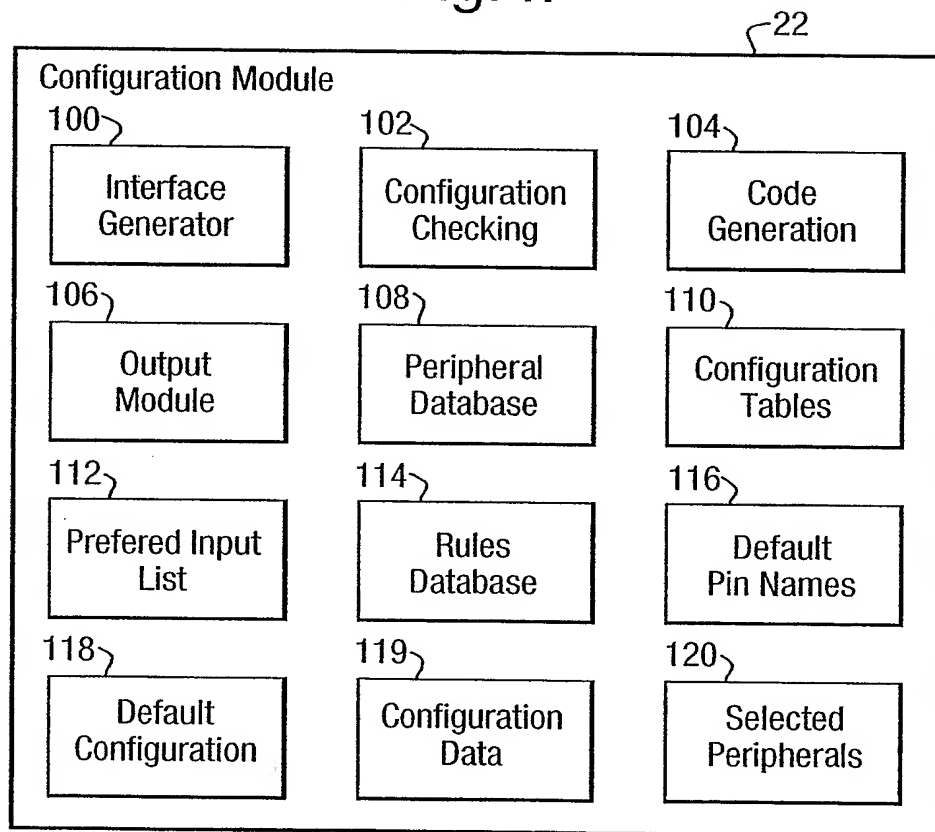
3/20

Fig.3.



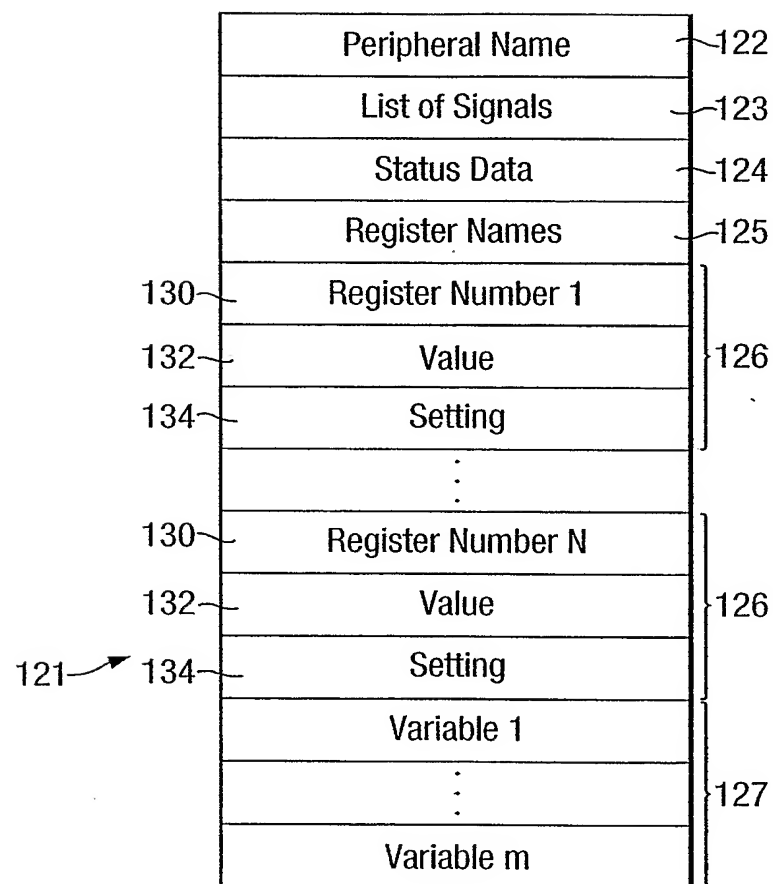
4/20

Fig.4.



5/20

Fig.5.



6/20

Fig.6.

135

136

Pin Number	Channel 0	Channel 1	Channel 2	Channel 3
62	WAKEUP	UARTB_RX	PWM1	WAKEUP
64	STREAM_ACK	UARTB_TX	PWM2	GPI025
65	STREAM_STS	GPI021	UARTB_RX	GPI027
66	STREAM_REQ	GPI022	UARTB_TX	GPI012

Fig.7.

141

142

143

144

120

140

141

142

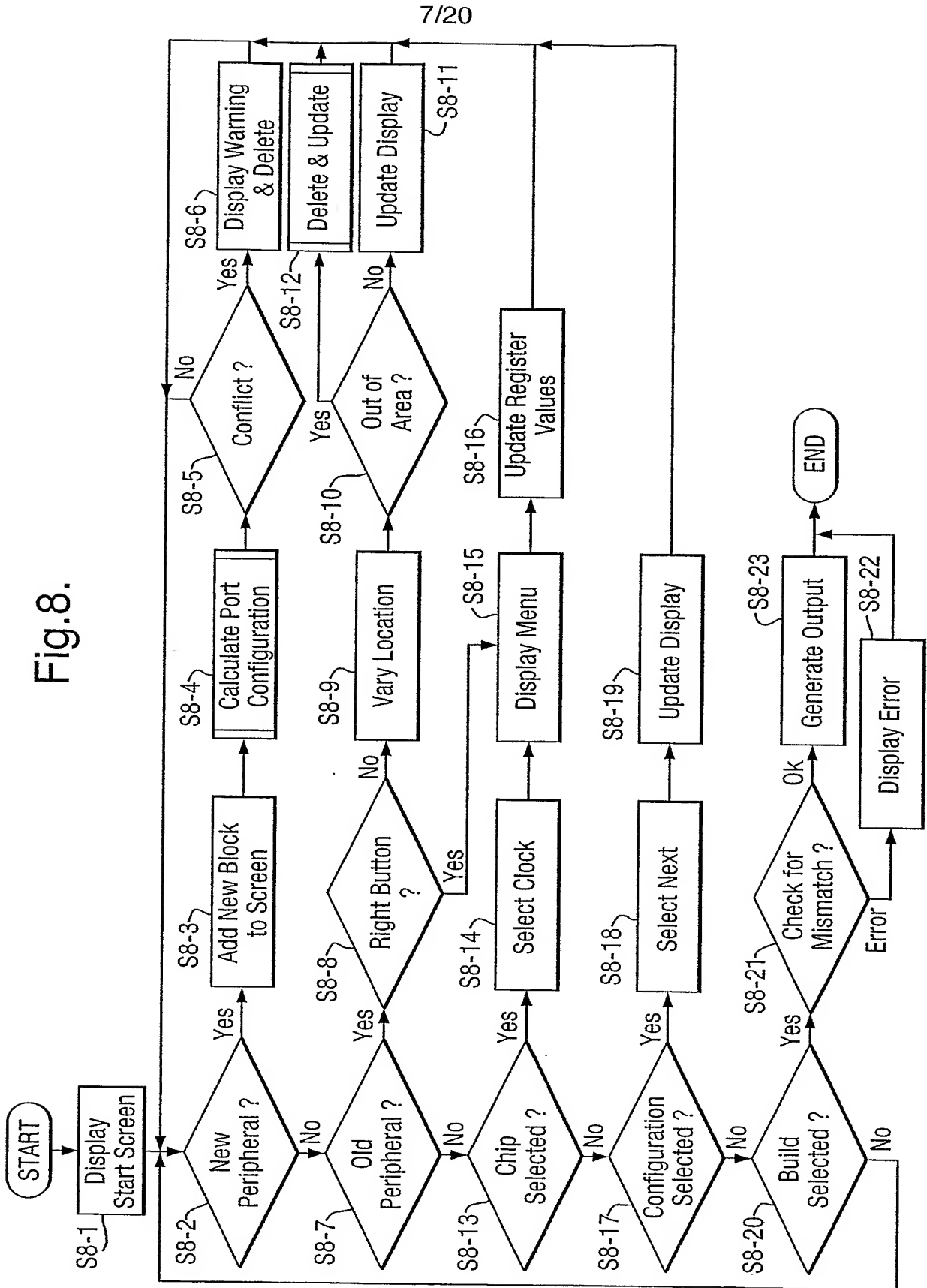
143

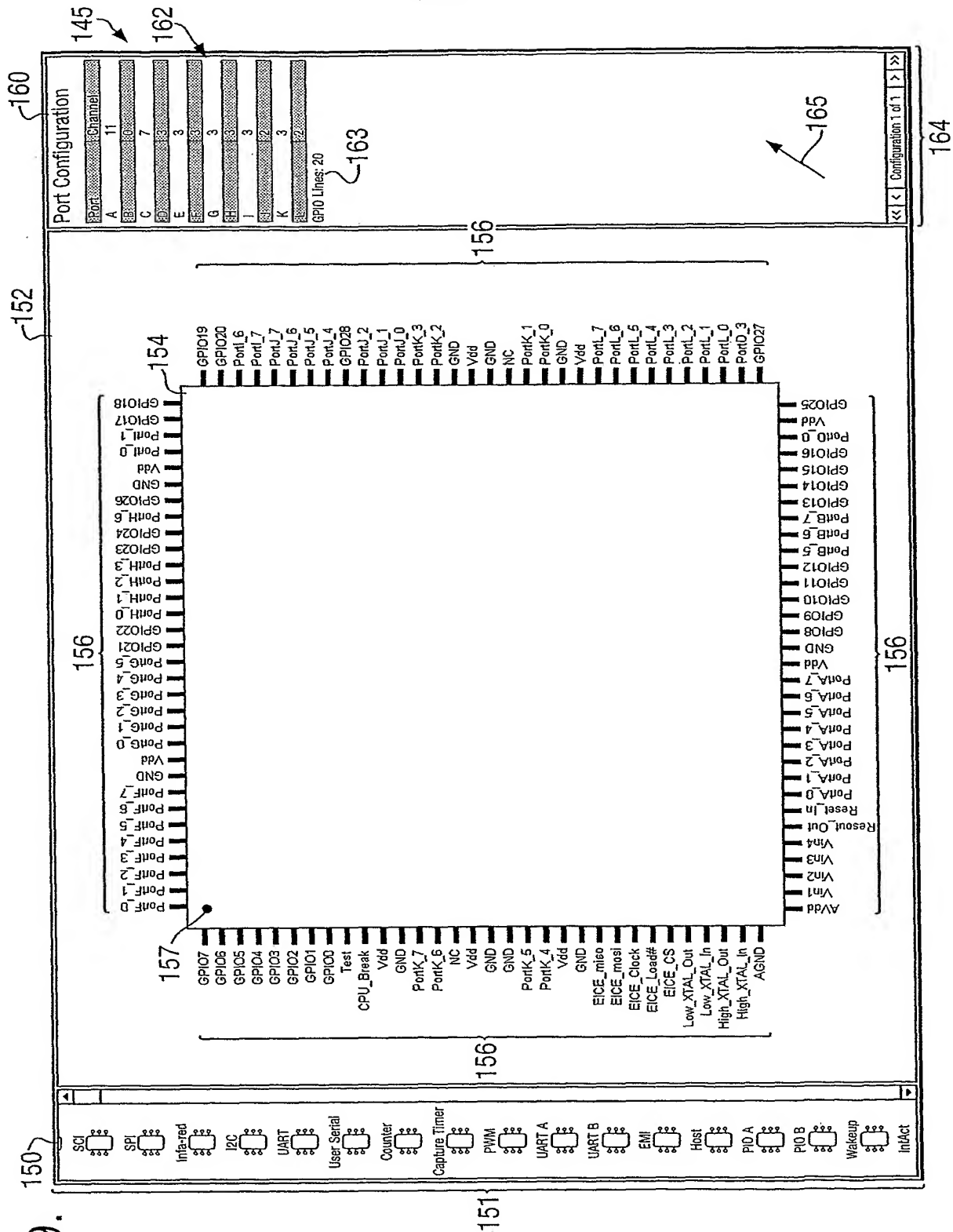
144

140

Peripheral Name	140
Register Values	
Variable Values	
Location	
⋮	
Peripheral Name	140
Register Values	
Variable Values	
Location	

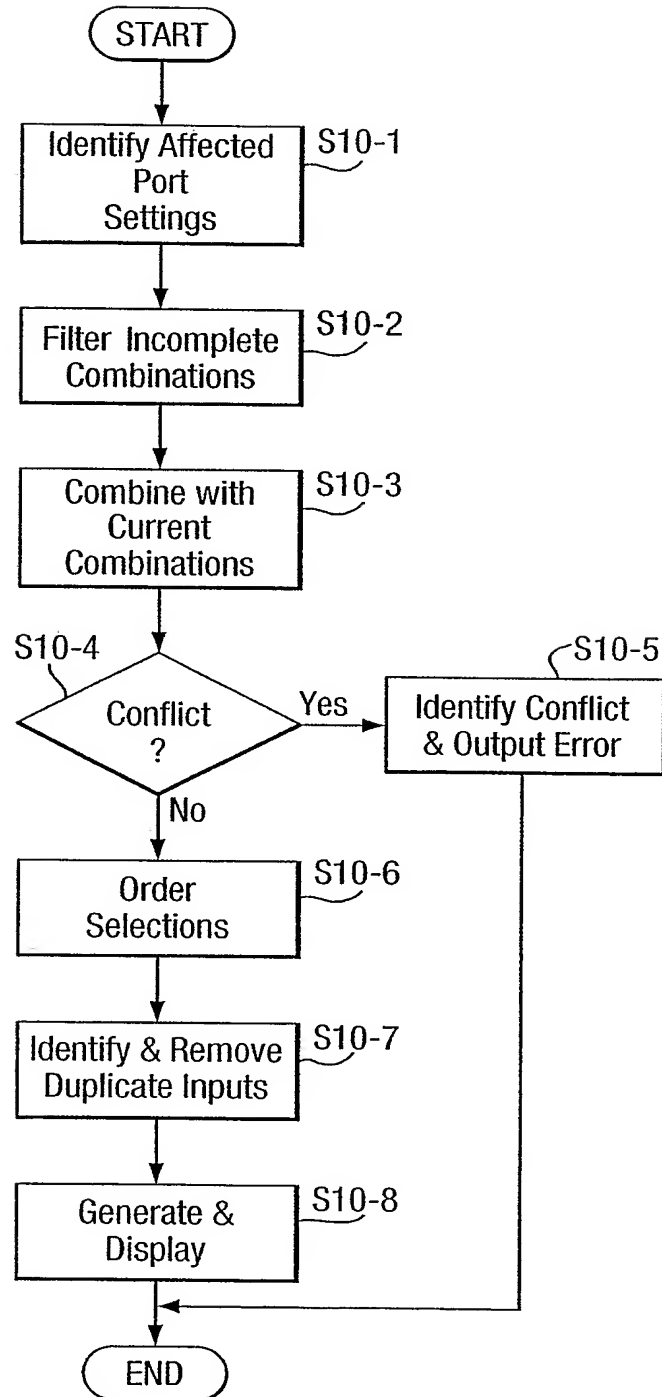
Fig.8.

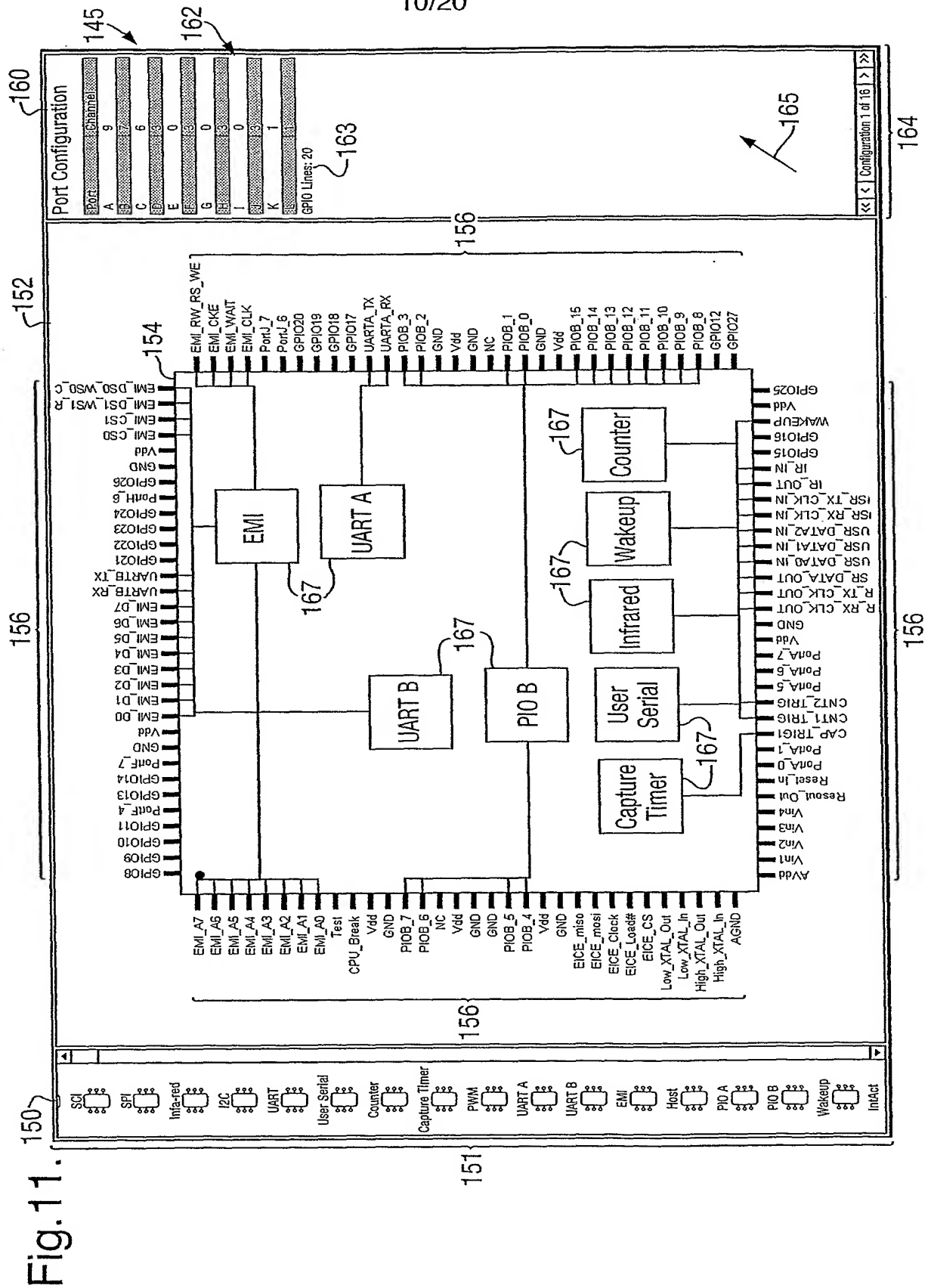




9/20

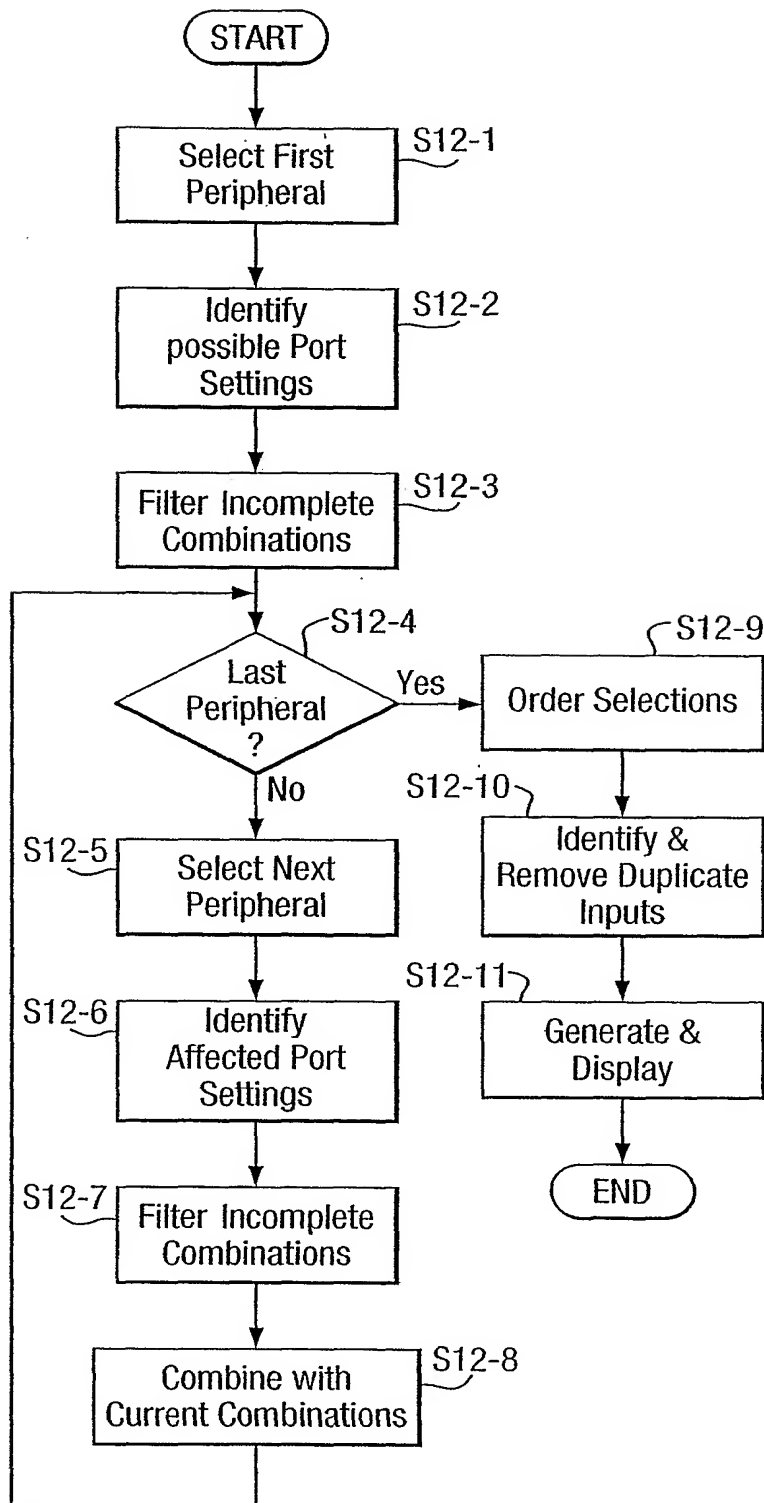
Fig.10.





11/20

Fig.12.



12/20

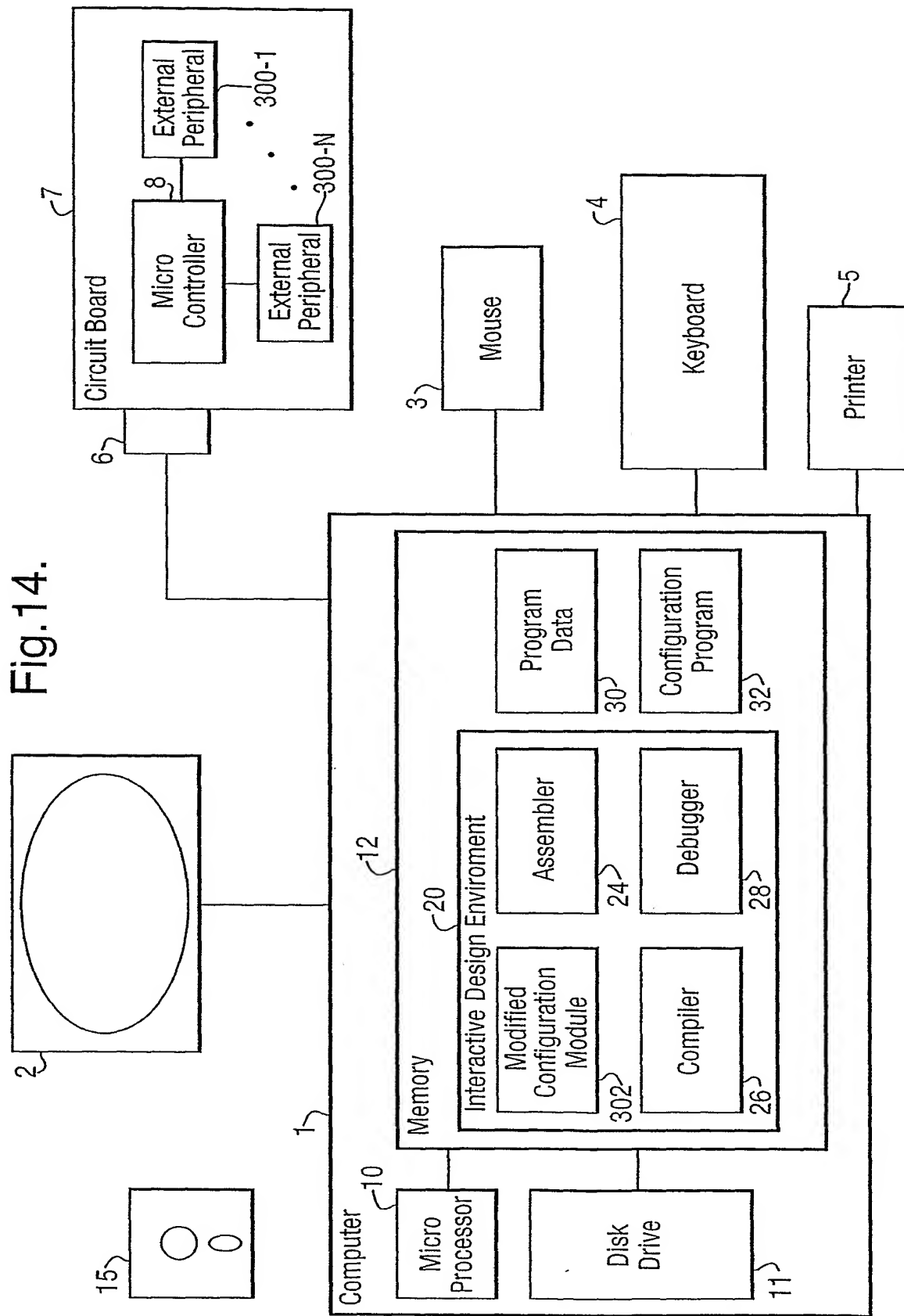
Fig.13.

The 'Clocks' window displays the following data:

Property	Value
Low Reference Clock	32.768 kHz
High Referenec Clock (MHz)	5
in_clk Frequency	High PLL
free_run_clk Frequency	in_clk/2
cpu_clk Frequency	free run clk/2
	free run clk/8
	free run clk/7
	free run clk/6
	free run clk/5
	free run clk/4
	free run clk/3
	free run clk/2
	free run clk

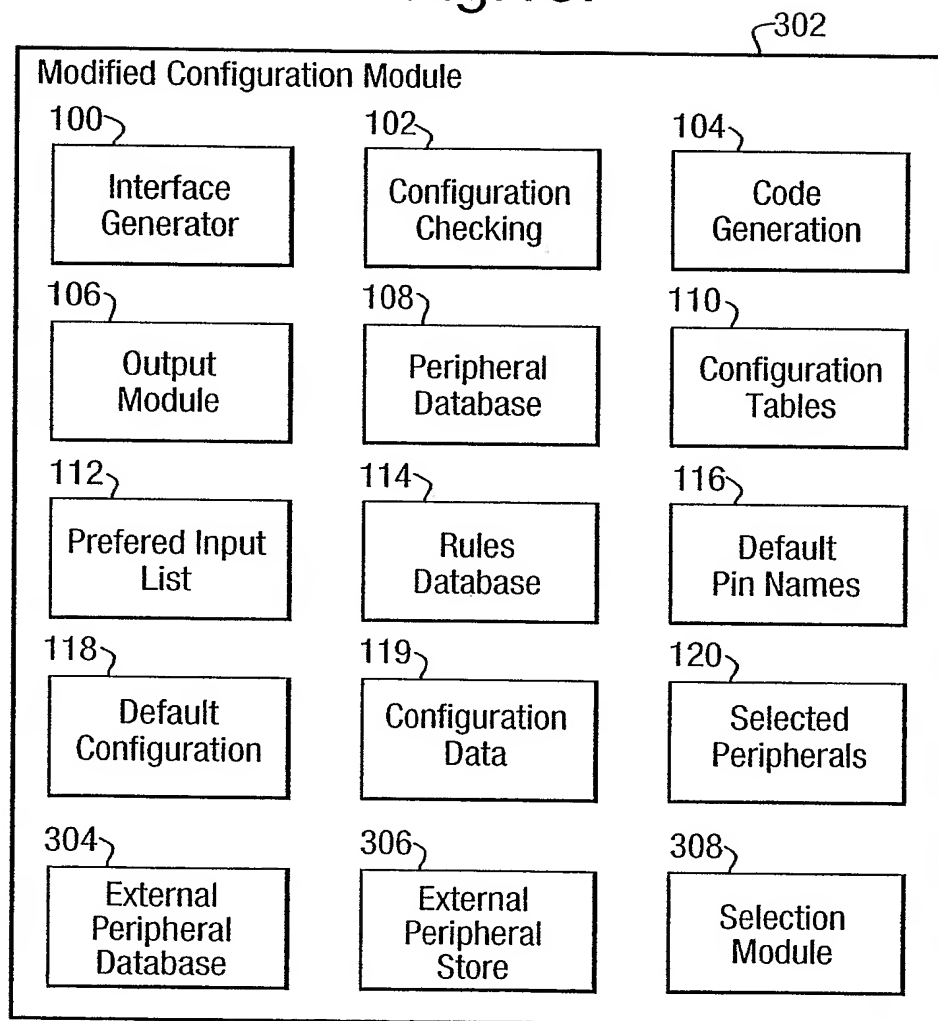
Labels in the diagram: 200 (main window), 202 (table area), 204 (list area), 206 (OK button), 207 (Cancel button), 165 (mouse cursor).

13/20



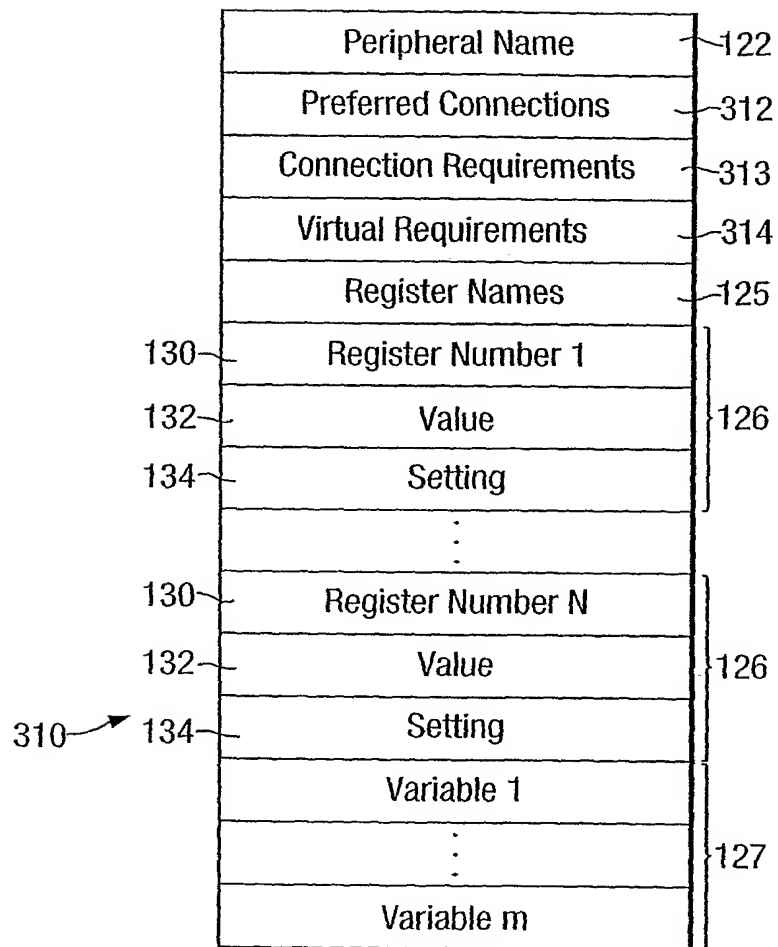
14/20

Fig.15.



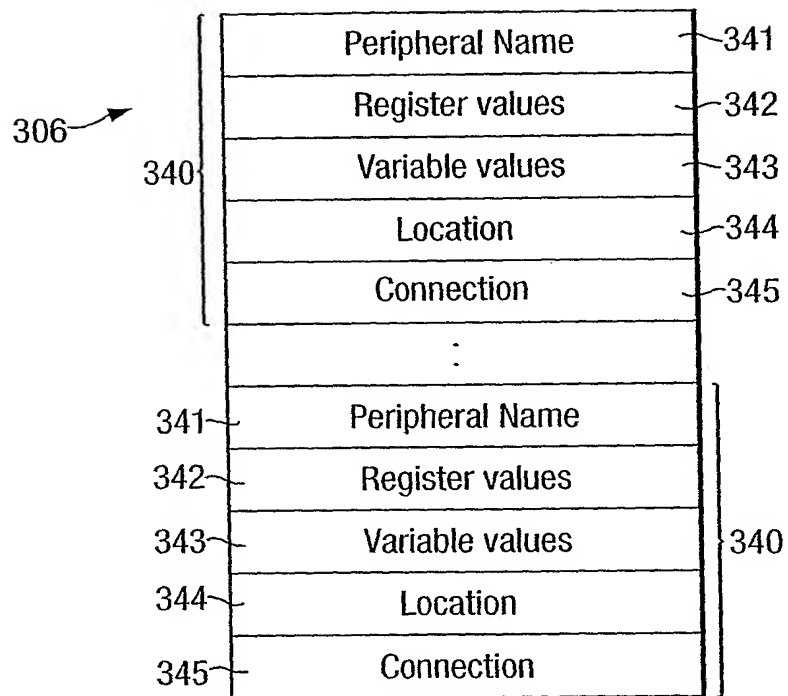
15/20

Fig.16.



16/20

Fig.17.



18/20

Fig.19.

